

VLT® POWERLINK MCA 123

The VLT® POWERLINK MCA 123 interface uses Standard-Fast-Ethernet, which through the POWERLINK timeslot mechanism has been enhanced with deterministic data transfer.



Ordering number
 Uncoated 130B1489
 Coated 130B1490

VLT® POWERLINK MCA 123 represents the second generation of fieldbus. The high bit rate of Industrial Ethernet is used and the full power of IT technologies to the automation world is available now for the factory world.

Ethernet is, when introduced in automation, still open and independent. POWERLINK does not only provide high performance real-time and time synchronisation features. Due to its CANopen-based communication models, network management and device description model offers much more than just a fast communication network.

With this architecture, all additional system features necessary for distributed automation systems are provided in a standardized manner. Therefore the integration of systems built up with devices from multiple manufacturers is easily possible.

The perfect solution for:

- Dynamic motion control applications
- Material handling
- Synchronisation and positioning applications



Feature	Benefit
Cycle-times down to 400µSec	Meets toughest real-time demands
High bandwidth Asynchronous IP data access	Multi-purpose bus
<ul style="list-style-type: none"> • Jitter < 1us • Velocity, position and marker synchronizing 	Maximum performance and precise timing
Integrated hub for daisy chaining	Supports any network topology
<ul style="list-style-type: none"> • No proprietary ASICs needed • License free 	Saves money
Multiplexed timeslot	Increases PLC & Bus performance
<ul style="list-style-type: none"> • Industry proven • Large installed base 	<ul style="list-style-type: none"> • Widely supported • Available and widely deployed
Plug-and-Play commissioning	Easy to learn and use
CANopen application layer	Backwards compatibility

Setup in Automation Studio

The VLT® POWERLINK MCA 123 option utilizes the features in the XDD file to provide a user-friendly interface. This means that the programmer can see the variables from the Frequency converter as channel names directly on screen. This reduces the risk of wrong assignments of I/O data and speeds up the configuration.

